

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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FJ
F-19

In re Patent Application of:
LIARDET ET AL.

Serial No. **Not Yet Assigned**

Filing Date: **Herewith**

For: **CIRCUIT AND METHOD FOR THE
SECURING OF A COPROCESSOR
DEDICATED TO CRYPTOGRAPHY**

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PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to the calculation of fees and examination of
the present application, please enter the amendments and
remarks set out below.

In the Drawings:

Submitted herewith is a request for a proposed
drawing modification to label FIGs. 1-3 prior art and to
provide various labels as indicated in red ink.

In the Claims:

Please cancel Claims 1-10.

Please add new Claims 11-34.

11. An electronic circuit for the securing of a
cryptography coprocessor comprising:

GK